2nd Sessional

University Institute of Engg. and Technology, Kurukshetra University Kurukshetra

Subject: Digital Electronics (EC-205) Class- ECE 3rd sem. (A&B) Max. Marks: 15 Time 1Hr:

Q1. All five parts of first question carrying equal marks (1*5=5)

a) Write one advantage of carry Look Ahead Adder?
b) How many inputs are used in Half Subtractor?
c) Write sum and carry expression for full adder?
d) Define Priority encoder and its advantage?
e) Give difference between sequential and combinational circuit?

Q2. Explain and design 4 bit parallel adder/subtractor? (5)

Q3. Implement logic function using Multiplexer: (5)

$F(A,B,C,D)=\sum m(1,2,5,6,7,8,10,12,13,15)$